

FIG. 1

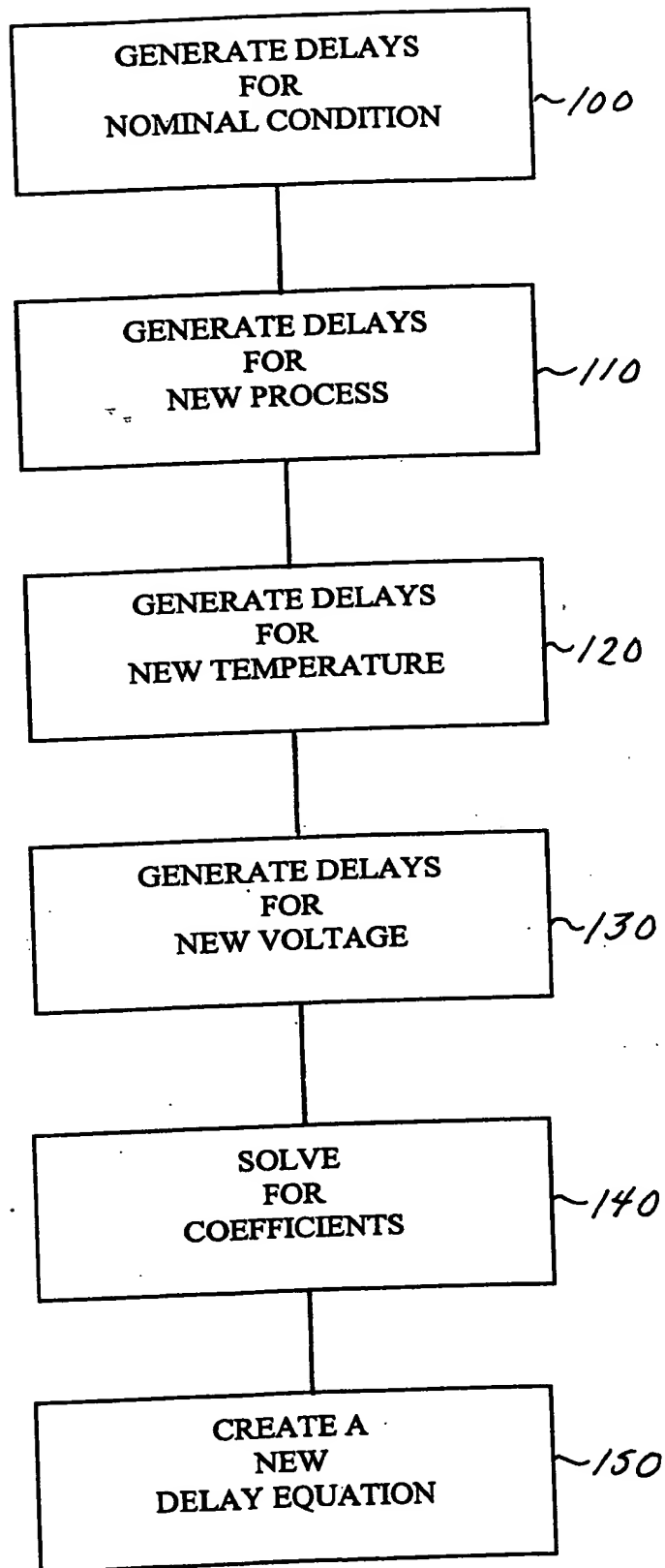


FIG. 2

FIG. 3 is a block diagram of a computer system 151, which includes a microprocessor 152, a clock 170, a RAM 156, an expansion RAM 158, a ROM 154, an input/output (I/O) 160, a display screen 162, an input device 184, a mass storage 168, a removable storage RW device 188, and a removable storage media 171. The microprocessor 152 is connected to the clock 170, the RAM 156, the expansion RAM 158, the ROM 154, the I/O 160, and the display screen 162. The RAM 156 is connected to the expansion RAM 158. The ROM 154 is connected to the I/O 160. The I/O 160 is connected to the mass storage 168, the removable storage RW device 188, and the input device 184. The removable storage RW device 188 is connected to the removable storage media 171. The display screen 162 is connected to the I/O 160. The input device 184 is connected to the I/O 160. The mass storage 168 is connected to the I/O 160. The removable storage RW device 188 is connected to the I/O 160. The removable storage media 171 is connected to the removable storage RW device 188.

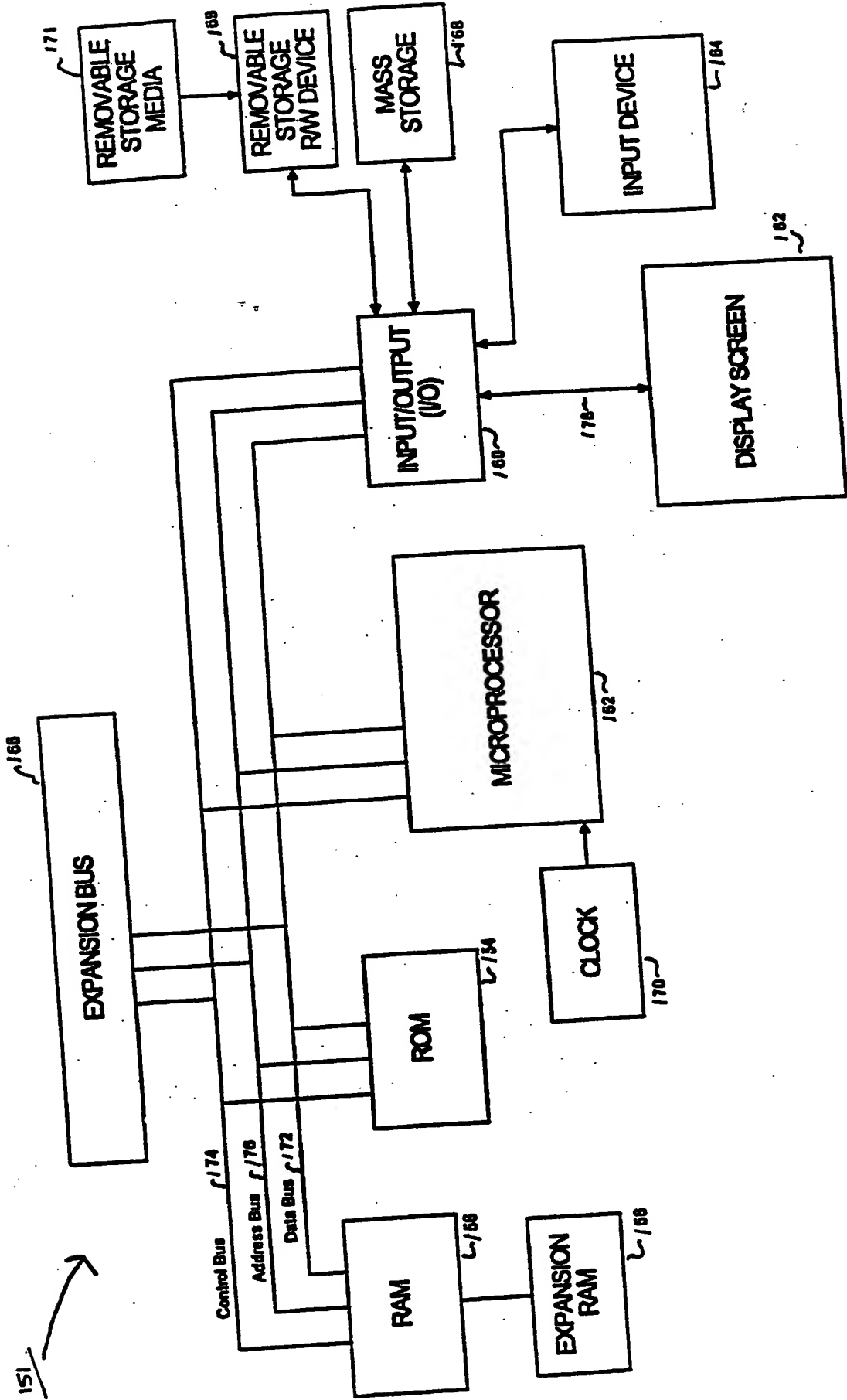


FIG. 3